

10/581549

AP20 Rec'd PCT/PTO 02 JUN 2006  
PATENT

Docket No.: 4590-531

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Patrick GREMILLET

U.S. Patent Application No. *not yet assigned*

Filed: *herewith*

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: Confirmation No.  
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For: METHOD OF GENERATING A DIGITAL SIGNAL THAT IS REPRESENTATIVE OF  
MATCH ERRORS IN AN ANALOG DIGITAL CONVERSION SYSTEM WITH TIME  
INTERLEAVING, AND AN ANALOG DIGITAL CONVERTER WITH TIME INTERLEAVING  
USING SAME

**INFORMATION DISCLOSURE STATEMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In accordance with the provisions of 37 C.F.R. 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the documents listed on the attached form PTO-1449. It is respectfully requested that the documents be expressly considered during the prosecution of this application, and that the documents be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement is being filed with the application and no certification or fee is required.

Please note this is a PCT application in the entry of the National Phase in the U.S. and copies of the references cited were transmitted by WIPO and are believed to be in the file of the above-identified application and readily available to the Examiner. Therefore it is believed that Applicants have met all requirements regarding duty of disclosure under 37 CFR 1.56. A copy of the foreign search report is

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attached for the Examiner's information. Acknowledgement and consideration of these documents are respectfully requested.

Respectfully submitted,

**LOWE HAUPTMAN & BERNER, LLP**

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**Date: June 2, 2006**

INFORMATION DISCLOSURE CITATION IN AN APPLICATION  (PTO-1449)				ATTY. DOCKET NO. 4590-531		U.S. APPLICATION NO. 07/281549 Not yet assigned	
				APPLICANT Patrick GREMILLET			
				FILING DATE herewith		GROUP	
U.S. PATENT DOCUMENTS							
EXAMINER'S INITIALS	PATENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE	
FOREIGN PATENT DOCUMENTS							
EXAMINER'S INITIALS	PATENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						Yes	No
	JP06152410	8/30/94	Japan (abstract)				
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)							
	Hummels D. M., et et al., Distortion Compensation for Time-Intrleved Analog to Digital Converters, IEEE 1996, Brussels, Belgium, June 4-6, 1996.						
EXAMINER			DATE CONSIDERED				

**EXAMINER:** Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.